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Substitute for form 1449/PTO		Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>		Application Number	Not Yet Assigned
		Filing Date	11/24/2003
		First Named Inventor	Chuan-Ping Hou
		Art Unit	Not Yet Assigned
		Examiner Name	Not Yet Assigned
		Attorney Docket Number	N1085-00192
Sheet 1	of 2		

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FOREIGN PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear
		Country Code ² Number ⁴ Kind Code ³ (if known)			

Examiner Signature	<i>Kenny Moshee</i>	Date Considered	02-07-05
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Sheet 2	of 2	Attorney Docket Number	N1085-00192

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
PHL	B	FU-LIANG YANG ET AL., "25 nm CMOS Omega FETs", 2002 IEEE, 4 pages	
PHL	C	S. MONFRAY ET AL., "50 nm - Gate All Around (GAA) - Silicon On Nothing (SON) - Devices: A Simple Way to Co-integration of GAA Transistors within bulk MOSFET process", 2002 IEEE, 2002 Symposium on VLSI Technology Digest of Technical Papers, 2 pages	
PHL	D	J.P. COLINGE ET AL., "SOI Devices for Sub-0.1 um Gate Lengths", 2002 IEEE, 2002 Proc. 23rd International Conference on Microelectronics (Miel 2002), Vol 1, 2 pages	
PHL	E	FU-LIANG YANG ET AL., "35 nm CMOS FinFETs", 2002 IEEE, 2002 Symposium on VLSI Technology Digest of Technical Papers, 2 pages	

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